

In the Claims:

Pursuant to 37 C.F.R. § 1.121(c) and the revised amendment practice effective July 30, 2003, please amend claims 6-8 as indicated herein. A complete listing of all the claims in the application is provided immediately below.

COMPLETE LISTING OF ALL CLAIMS IN THE APPLICATION

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1. (original) A frequency-sensitive electrical circuit, comprising:
first and second inputs;
at least one transformer circuit having a first winding connected to the first input and a second winding connected to the second input;
a first load connected in parallel to the first winding;
a second load connected in parallel to the second winding;
first and second outputs connected to the first and second windings, respectively; and
a capacitor connected between the first and second outputs.
 2. (original) The circuit of claim 1, wherein a signal carrying both voice and data information is received at the first and second inputs.
 3. (original) The circuit of claim 1, wherein the circuit filters a lower-frequency portion of a signal received at the first and second inputs.
 4. (original) The circuit of claim 1, wherein the circuit reduces the distortion of a signal received at the first and second inputs and delivered at the first and second outputs.
 5. (original) The circuit of claim 1, further comprising a third output, connected via a second capacitor to the first input, and a fourth output, connected via a third capacitor to the second input.

- A1
6. (currently amended) A frequency-sensitive electrical circuit, comprising:
a first stage having first and second inputs and first and second outputs, the first stage comprising
a parallel-connected first inductor and first resistor, connected between the first input and first output,
a parallel-connected second inductor and second resistor, connected between the second input and second output, the first and second inductors being inductively coupled, and
a first capacitor connected between the first and second outputs.
7. (currently amended) The circuit of claim 6, further comprising:
a second stage having first and second inputs and first and second outputs, the ~~first~~ second stage comprising
a parallel-connected ~~first~~ third inductor and ~~first~~ second capacitor, connected between the first input and first output of the second stage,
a parallel-connected ~~second~~ fourth inductor and ~~second~~ third capacitor, connected between the second input and second output of the second stage, the ~~first~~ third and ~~second~~ fourth inductors being inductively coupled, and
a ~~third~~ fourth capacitor connected between the first and second outputs of the second stage,
wherein the first and second outputs of the second stage ~~are operatively connected to form~~ the first and second inputs of the first stage, respectively.
8. (currently amended) The circuit of claim 6, further comprising:
a ~~third~~ second stage having first and second inputs and first and second outputs, the ~~first~~ second stage comprising
a ~~first~~ third inductor connected between the first input and first output of the second stage,
a ~~second~~ fourth inductor connected between the second input and second output of the second stage, the ~~first~~ and second inductors being inductively coupled, and

a second capacitor connected between the first and second outputs of the second stage, wherein the first and second outputs of the ~~third~~ second stage are ~~operatively connected to~~ form the first and second inputs of the first stage, respectively.

9. (original) The circuit of claim 6, wherein a signal carrying both voice and data information is received at the first and second inputs.

10. (original) The circuit of claim 6, wherein the circuit filters a lower-frequency portion of a signal received at the first and second inputs.

11. (original) The circuit of claim 6, wherein the circuit reduces the distortion of a signal received at the first and second inputs and delivered at the first and second outputs.

12. (original) The circuit of claim 6, further comprising a third output, connected via a second capacitor to the first input, and a fourth output, connected via a third capacitor to the second input.

13. (original) A telecommunications signal splitter, comprising:
first and second signal inputs;
at least one transformer circuit having a first winding connected to the first signal input and a second winding connected to the second signal input;
a first load connected in parallel to the first winding;
a second load connected in parallel to the second winding;
first and second outputs connected to the first and second windings, respectively; and
a capacitor connected between the first and second outputs.

14. (original) The circuit of claim 13, wherein a signal carrying both voice and data information is received at the first and second inputs.

15. (original) The circuit of claim 13, wherein the circuit filters a lower-frequency portion of a signal received at the first and second inputs.

16. (original) The circuit of claim 13, wherein the circuit reduces the distortion of a signal received at the first and second inputs and delivered at the first and second outputs.

17. (original) The circuit of claim 13, further comprising a third output, connected via a second capacitor to the first input, and a fourth output, connected via a third capacitor to the second input.